Amendments to the Claims:

The following claims will replace all prior versions of the claims in this application (in the unlikely event that no claims follow herein, the previously pending claims will remain):

1.   (**Currently Amended**) A network-storage apparatus for high-speed streaming data transmission through a network, the apparatus comprising:

an internal peripheral device bus separated from a peripheral device bus outside the network-storage apparatus, for transmitting data between devices inside the network-storage apparatus;

a peripheral device bus bridge for transferring bus transaction from a host processor to the internal peripheral device bus and transferring bus transaction for a host processor executing inside the network-storage apparatus or a main memory to a bus bridge;

a disk controller for controlling a plurality of disc storage connected to the network-storage apparatus and managing reading and writing data from and to the disc storage;

a peripheral memory for storing transmitted data between the disc storage and the network;

a peripheral memory controller for controlling the peripheral memory and storing or outputting the transmitted data between the disc storage and the network; and;

a TCP/IP offload engine (TOE) for TOE for reading data to be transmitted to the network from the peripheral memory, constructing the data in the form of a packet including information for network transmission, transmitting the packet to the network, and storing the data received from the network in the peripheral memory through the peripheral memory controller; and

a media access control unit (MAC) situated between the TOE and the network;

wherein the network storage apparatus stores the streaming data received through network on the disk storage in the form of zero copy and transmits the streaming data stored on the disk through the network in the form of zero copy, between the plurality of disk storage of an internet server computer system and a network, wherein the peripheral device bus is a PCI bus and the peripheral device bus bridge roles a PCI bridge;

wherein the TOE is situated between the peripheral device bus and the MAC such that streaming data transmitted to and from the network passes via the MAC and the TOE to the

peripheral device bus, the TOE communicating with the MAC directly and not over the peripheral device bus which is shared with the disk controller and peripheral memory controller, thereby enabling high-speed streaming data through the network; and

wherein the TOE includes a DSB table having information on packet data to be transferred to disk storage immediately among data packets received from the network.

2.   (Canceled).

3.   (Previously Presented) The apparatus of claim 1, wherein the disk controller is connected to a plurality of disc storages in parallel through a disk interface bus and accesses the data in a pipelined manner.

4.   (Original) The apparatus of claim 1, wherein the disk controller reads and writes data from and to a plurality of disc storages in a stripping manner.

5.   (Original) The apparatus of claim 1, wherein the peripheral memory controller constructs a memory table so as to cache data transmitted from and to the network.

6.   (Previously Presented) The apparatus of claim 1, wherein the peripheral memory controller is provided a register for indicating size of the peripheral memory inside the peripheral memory controller, and transmits data in a DMA manner.

7.   (Original) The apparatus of claim 1, wherein the peripheral memory controller deletes contents of a memory table thereof when finishing accessing to the peripheral memory.

8.   (**Currently Amended**) The apparatus of claim 1, wherein the TOE ~~creates a DSB table having information on packet data to be transferred to the disk immediately among data packets received from the network, transmitting~~is configured to transmit a data packet received from the network to the peripheral memory to store the data packet if the data packet storable in

the disk storage has information matching DSB, and transmitting a data packet to a general network stack otherwise.

9.   (Original) The apparatus of claim 1, wherein the TOE reads data to be transmitted to the network from the peripheral memory, constructs the data in the form of a packet and transfers the data packet to the network when the data to be transmitted is stored in the peripheral memory and the TOE receives a data transmission instruction from a host processor.